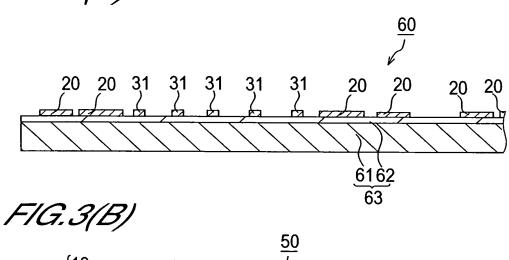
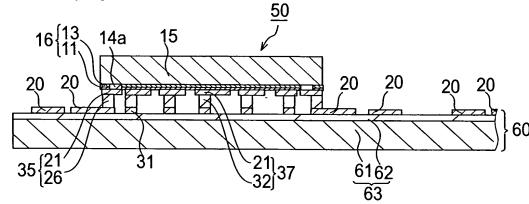
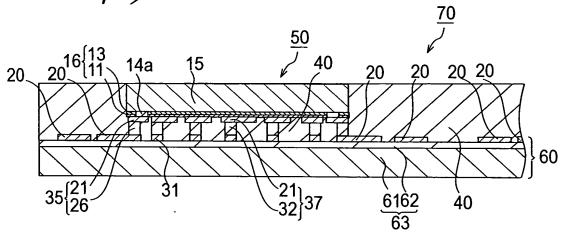


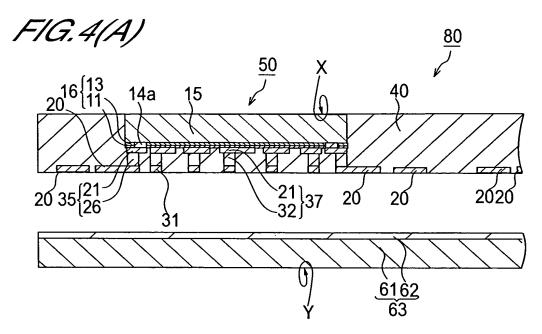
## FIG.3(A)

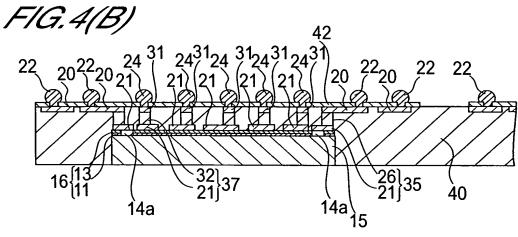






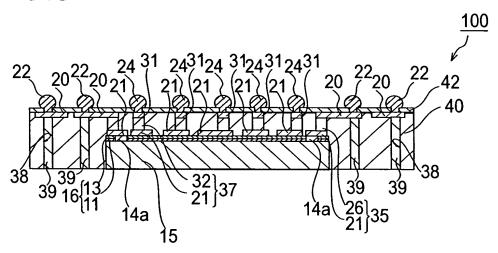




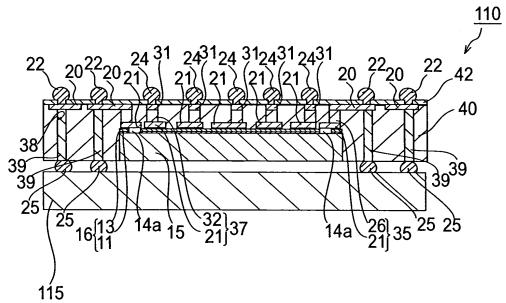


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FIG.5

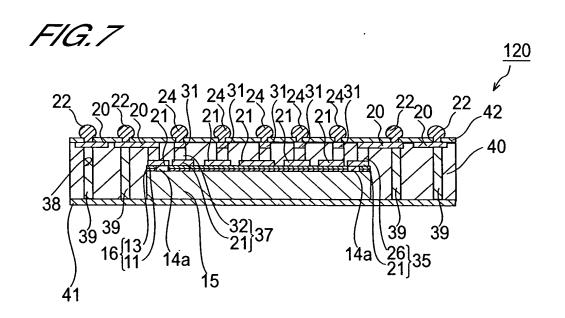


## FIG.6



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APPLICANT: Shigeru YAMADA
TITLE: SEMICONDUCTOR DEVICE...

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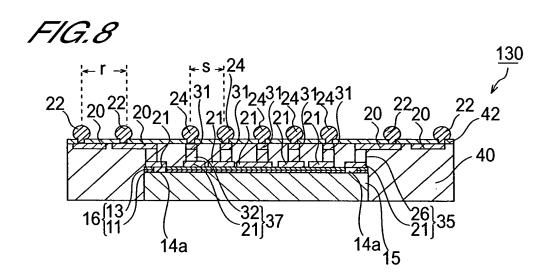


FIG.9

